SYLLABUS FOR THE POST OF LECTURER (TECHNICAL)

	SYLLABUS FOR COMPUTER SCIENCE AND ENGINEERING
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	Problem Solving using C: Design of algorithms for solving problems and use of C language features
	Problem Solving using C: Design of algorithms for solving problems and design pointers and like expressions, branching and looping, arrays and structures, functions, recursion, pointers and like expressions, branching and looping, arrays and structures, functions, recursion, pointers and like expressions, branching and looping, arrays and structures, functions, recursion, pointers and
	ike expressions, branching and looping, arrays and structures, functions, recursive properties algorithms lynamic memory allocation, preprocessor directives, files etc. for implementation of these algorithms lynamic memory allocation, preprocessor directives, files etc. for implementation of these algorithms lynamic memory allocation, preprocessor directives, files etc. for implementation of these algorithms
+	dynamic memory allocation, preprocessor directives, files etc. for implementation and languages. Object Oriented Programming using C++: Features of object oriented programming languages. Object Oriented Programming using C++: Features of object oriented programming languages.
	Classes and objects, inheritance, compiler time and run time personal
	interfaces, exception handling, class templates
	Interfaces, exception handling, class templates Internet Programming: Java language features, use of AWT and SWING package, event driven Internet Programming: Java language features, use of AWT and SWING package, event driven Internet Programming: Java language features, use of AWT and SWING package, event driven
	Internet Programming: Java language features, use of AWT and SWING Pathogain Java language features, use of AWT and SWING Pathogain Java language features, use of AWT and SWING Pathogain Java language features, use of AWT and SWING Pathogain lang
	SOAP, XML, XSL, JSP and PHP
	Di : 1 Contamo: Transistor Fogic family, Simplification of Boolean Fundament
	design, synchronous sequential logic design, counters, registers
	design, synchronous sequential logic design, counters, registers Computer Organization: Architecture of 8086/8088 microprocessor, instruction set architecture and RISC CISC, memory technology, IO subsystem.
	Computer Organization: Architecture of 8086/8088 microprocessor, instruction set and addressing modes, assembly language programming, RISC, CISC, memory technology, IO subsystem.
	addressing modes, assembly language p
	pipelining Data structures: Representation and implementation of linear data structures like linear lists, stacks, Para structures: Representation and implementation of
	Data structures: Representation and implementation of finear data structures: Representation and implementation of queues, dynamic memory storage management techniques, representation and implementation of queues, dynamic memory storage management techniques, representation and implementation of queues, dynamic memory storage management techniques, representation and implementation of queues, dynamic memory storage management techniques, representation and implementation of queues, dynamic memory storage management techniques.
	queues, dynamic memory storage management techniques, representation and surface queues, dynamic memory storage management techniques, representation and surface graph algorithms graphs, trees, binary search trees, height balanced trees, searching and sorting, graph algorithms graphs, trees, binary search trees, may flow) and tree algorithms (traversals, searching,
	graphs, trees, binary search tress, height balanced trees, searching and sorting, graphs, trees, binary search trees, height balanced trees, searching and servings of traversals, searching, (traversal, shortest path, spanning tree, max flow) and tree algorithms (traversals, searching,
	(traversal, shortest paul, spaining acc, man
	Successors) Operating System: Architecture, process management, process synchronization and inter process Operating System: Architecture, process management and memory management, System V
7	Operating System: Architecture, process management, process synchronization of the communication, UNIX system calls for process management and memory management, System V
	communication, UNIX system cans for process many
	IPC, Files and Directories. Microprocessor and microcontrollers: 8085 Microprocessor, parallel data transfer using 8155 - Microprocessors and microcontrollers: 8085 Microprocessor parallel data transfer using 8155 - Microprocessor and microcontrollers: 8085 Microprocessor parallel data transfer using 8155 - Microprocessor and microcontrollers: 8085 Microprocessor parallel data transfer using 8155 - Microprocessor and microcontrollers: 8085 Microprocessor parallel data transfer using 8155 - Microprocessor and microcontrollers: 8085 Microprocessor parallel data transfer using 8155 - Microprocessor and microcontrollers: 8085 Microprocessor parallel data transfer using 8155 - Microprocessor parallel data transfer
8	Microprocessors and microcontrollers: 8085 Microprocessor, parameter data of the microprocessor of the micropr
	DMA transfer using 8257 DMA controller, system deargn as techniques
	DMA transfer using 8257 DMA controller, system details Disk Controller - CRT controller, microprocessor interfacing techniques Disk Computing: Processors and hardware units in an embedded system, architecture. Embedded Computing: Processors and hardware units in an embedded system, architecture. Application-Specific Circuits, FPGA.
9	Embedded Computing: Processors and hardware units in an embedded system. defined the controller of the
	instruction set and programming with 8051 micro controller, Application Spectral Spectra Spectral Spectra Spectra Spectra Spectra Spectra
	ARM-based System on a Chip, Network on Chip, Maraway
	time operating systems, embedded application development in TCP/ID orghitectures, data encoding, data
10	Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network: Data transmission concepts, OSI and TCP/IP architectures, data encountries of the Computer Network (Computer Network) and the Computer Network (Computer Network) and the Computer Network (Computer Netwo
	link control, Medium Access Control, Touring angertain
	Layer (FTP,SMTP,SNMP,DNS,HTTP) Layer (FTP,SMTP,SNMP,DNS,HTTP) Formal Language and Automata Theory: Deterministic and non-deterministic finite automata. Formal Language and Automata Theory: Deterministic and non-deterministic finite automata.
11	Formal Language and Automata Theory: Deterministic and down automaton numping lemma,
	Context-Free Grammars (simplification, normal feeting, parties of recursive and recursively
	properties of CFL), Turing Machines, Undecidability, properties of recursive and recursive function
	animorable languages liniversal runing machines, p
	theory, oracle computation, Chomsky Hierarchy
12	theory, oracle computation, Chomsky Hierarchy Database Management System: Relational data model, relational languages, file organization, Database Management System: Relational data model, relational languages, file organization, database design, concurrency control and recovery, parallel
12	Database Management System: Relational data model, relational languages, and parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization, database design, concurrency control and recovery, parallel query processing, query optimization and processing and transformation of XML document schema, object
	and distributed database. Storage, querying and transfer
	databases, advanced transaction processing databases, advanced transaction processing databases, advanced transaction processing databases, advanced transaction processing
1	databases, advanced transaction processing Compiler Design: Phases of a compiler, lexical analysis, top down and bottom up parsing (recursive compiler Design: Phases of a compiler, lexical analysis, top down and bottom up parsing (recursive compiler Design: Phases of a compiler, lexical analysis, top down and bottom up parsing (recursive compiler Design: Phases of a compiler, lexical analysis, top down and bottom up parsing (recursive compiler Design: Phases of a compiler, lexical analysis, top down and bottom up parsing (recursive compiler Design: Phases of a compiler, lexical analysis, top down and bottom up parsing (recursive compiler Design: Phases of a compiler Design: Phase
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	generation and optimization, target code generation
-	generation and optimization, target code generation generation and optimization, target code generation Design and Analysis of Algorithms: Asymptotic analysis of complexity bounds, algorithm design by Design and Analysis of Algorithms: Asymptotic analysis of complexity bounds, algorithm design by
1	Design and Analysis of Algorithms: Asymptotic analysis of complexity bounds methodologies, red brute-force, greedy, branch-and-bound, backtracking and dynamic programming methodologies, red brute-force, greedy, branch-and-bound, backtracking in linear time, amortized complexity, median and
	brute-force, greedy, branch-and-bound, backtracking and dynamic programming brute-force, greedy, branch-and-bound, branch-and-bound-bound-bound-bound-branch-and-bound-bound-bound-bound-bound-bound-bound-bound-bound-bound-bound-bound-b
	descriptions tractable and illuactable problems, seems
	randomized algorithms, evolutionary algorithms randomized and intractionary algorithms
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1	Software Engineering: Software life cycle models, software requirement software design, software testing and quality management, software project management software design, software testing and quality management.
	software design, software testing and quarry managers Page 1 of 2
	Software Engineering: Software into system an agement, software project management software design, software testing and quality management, software project management page 1 of 2
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16	Distributed Computing: Architectural models, logical clocks, mutual exclusion, distributed deadlock	
16		
	detection, distributed objects and remote invocation, distributed transactions	
17	Data Mining: Data Preprocessing, Association and Correlation Analysis, Clustering Algorithms and	
	Cluster Analysis, Classification, Applications	
18	Artificial Intelligence: Scope of AI, State space search, Knowledge Representation and Reasoning,	
	Handling uncertainty and learning	
19	Graph Theory: Basic concepts, Cut vertices, bridges and blocks, auto Orphism groups, Trees and	
	connectivity, Eulerian and Hamiltonian graphs, Coloring and planar graphs, Matching, factors,	
	decomposition and domination, Extremal Graph theory	

